TRANSMITTER PINOUT CODE P1

Connector: No Connector--board only

Pin	Function
1	J1 Lock Detect
2	J2 Ground
3	J3 SCLOCK - Serial Clock Input (Data clocked into the 32-bit shift register on the CLK rising edge)
4	J4 SDATA (Serial data loaded MSB first with the three LSBs as the control bits)
5	J5 LE1-Load Enable, CMOS Input (If LE goes high, data stored in the shift register is loaded into the register)
6	J6 Power Detect
7	J7 DC Power Return
8	J8 DC Power In + 5 VDC

DRAWING	qsx_pcp1.docx		
REVISION	B		
DRAWN	MRE	DATE	08-22-2018
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